###### Assignment Cover Sheet

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| School | SVUCA – Summer 2015 | **SCORE** |
| Module Code | CE450 – Computer Architecture I |
| Student ID | 150201133 |
| Student Name | DOAN QUOC SY **VO** |
| Assignment Title/Number | Unit 10 – Chapter 4b Homework | |

# Questions

1. Explain what is “pipelined instructions” and how does it work? Use an example to compare the non-pipeline and pipeline difference?

2. What are the potential hazard problems in the pipeline instructions and how to solve each problem?

3. What are “forwarding” and “stall”? When and how do we use them?

4. What is the branch hazard? How to fix the branch hazard?

5. What is exception? How does MIPS handle the exception problem?

# Answers